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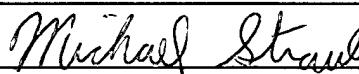
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		Application Number	10/637,844
		Filing Date	August 8, 2003
		First Named Inventor	Hui JIN
		Group Art Unit	2183
		Examiner Name	Not yet assigned
Total Number of Pages in This Submission		Attorney Docket Number	Flarion-78APP1 (103)

ENCLOSURES *(check all that apply)*

<input type="checkbox"/> Fee Transmittal Form <input type="checkbox"/> Fee Attached	<input type="checkbox"/> Assignment Papers <i>(for an Application)</i> <input type="checkbox"/> Drawing(s) <input type="checkbox"/> Licensing-related Papers <input type="checkbox"/> Petition <input type="checkbox"/> Petition to Convert to a Provisional Application <input type="checkbox"/> Power of Attorney, Revocation Change of Correspondence Address <input type="checkbox"/> Terminal Disclaimer <input type="checkbox"/> Request for Refund <input type="checkbox"/> CD, Number of CD(s) _____	<input type="checkbox"/> After Allowance Communication to Group <input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences <input type="checkbox"/> Appeal Communication to Group <i>(Appeal Notice, Brief, Reply Brief)</i> <input type="checkbox"/> Proprietary Information <input type="checkbox"/> Status Letter <input checked="" type="checkbox"/> Postcard Receipt <input type="checkbox"/> Other Enclosure(s) <i>(please identify below):</i> References CA-CM
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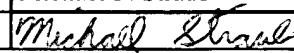
SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm or Individual name	Michael P. Straub (Reg. No. 36,941)
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IN THE UNITED STATES
PATENT AND TRADEMARK OFFICE

Attorney Docket No.: **Flarion-78APP1 (103)**

Applicant: **Hui Jin et al.**

Serial No.: **10/637,844**

Filing Date: **August 8, 2003**

Title: **METHODS AND APPARATUS FOR DATA TRANSMISSION IN A
BLOCK-COHERENT COMMUNICATION SYSTEM**

Examiner: **Not yet assigned**

Group Art Unit: **2183**

Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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Information Disclosure Statement Transmittal

The applicants respectfully request that the references listed on the attached PTO/SB/08A be considered in the examination of the above-identified application. A copy of each of these references, except for U.S. patents and patent application publications is enclosed. (See the notice, "Information Disclosure Statements May Be Filed Without Copies of U.S. Patents and Published Applications in Patent Applications Filed After June 30, 2003," Pre-OG Notices (July 11, 2003).)

The applicants reserve the right to establish that any of the references listed on the attached PTO/SB/08A are not prior art to the above-captioned application.

Since a first Office Action on the merits has not yet been received, the applicants assume that this Information Disclosure Statement should be considered under 37 C.F.R.

§§ 1.97(b)(3). Accordingly, it is believed that no fee is due.

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Respectfully submitted,

Dated: January 18, 2005

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Michael P. Straub Reg. No.



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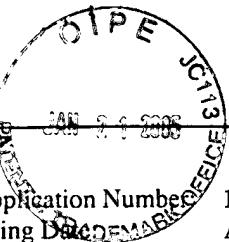
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Michael P. Straub Reg. No.

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Patent and Trademark Office
**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
(use as many sheets as necessary)

 Application Number: 10/637,844
 Filing Date: August 8, 2003
 First Named Inventor: Hui Jin
 Group Art Unit: 2183
 Examiner Name: Not yet assigned

Sheet 1 of 1 Attorney Docket No.: Flarion-78APP1 (103)

U.S. PATENT DOCUMENTS

Examiner Initials ²	Cite No. ¹	U.S. Patent Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines where relevant Passages or Figures appear
	AA	3,542,756	Nov 24 1970	Gallager	
	AB	3,665,396	May 23 1972	Forney, Jr	
	AC	4,295,218	Oct 13 1981	Tanner	
	AD	5,157,671	October 20, 1992	Karplus	
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Examiner Initials ²	Cite No. ¹	Foreign Patent Document Office ³ Number ⁴	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶

Examiner Signature	Date Considered

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>				Application Number: 10/637,844
				Filing Date: August 8, 2003
				First Named Inventor: Hui Jin et al.
				Group Art Unit: 2631
				Examiner Name: Not yet assigned
Sheet	2	of	2	Attorney Docket No.: Flarion-78APP1 (103/1)

OTHER REFERENCES - NON-PATENT LITERATURE DOCUMENTS				
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume, issue number(s), publisher, country, where published, source		
	CA	Richardson et al. The capacity of low-density parity-check codes under message-passing Decoding, IEEE Transactions on Information Theory; pages: 599-618, February 2001, (same inventor) whole document.		
	CB	Paranchych et al. Performance of a digital symbol synchronizer in cochannel interference and noise, IEEE Transactions on Communications, pages: 1945-1954; Nov. 2000, whole document.		
	CC	NN77112415. Digital Encoding of Wide Range Dynamic Analog Signals, IBM Tech. Disclosure Bulletin, November 1, 1997, VOL. No. 20; ISSUE No. 6; Pages 2415-2417, whole document		
	CD	NN9210335. Hierarchical Coded Modulation of Data with Fast Decaying Probability Distributions, IBM Tech. Disclosure Bulletin, October 1992, VOL. No. 35; ISSUE No. 5; Pages 335-336, whole document.		
	CE	Sorokine, V. et al. Innovative coding scheme for spread-spectrum communications, The Ninth IEEE International Symposium on Indoor and Mobile Radio Communications, pages: 1491-1495, Vol. 3; September 1998, whole document.		
	CF	T. Moors and M. Veeraraghavan, "Preliminary specification and explanation of Zing: An end-to-end protocol for transporting bulk data over optical circuits", pp. 1-55 (May 2001).		
	CG	T. Richardson and R. Urbanke, "The Capacity of Low-Density Parity-Check Codes under Message-Passing Decoding", pp. 1-44 (March 2001).		
	CH	T. Richardson, A. Shokrollahi, R. Urbanke, "Design of Capacity-Approaching Irregular Low-Density Parity-Check Codes", pp. 1-43 (March 2001).		
	CI	T. Richardson and R. Urbanke, "An Introduction to the Analysis of Iterative Coding Systems", pp. 1-36.		
	CJ	Saied Hemati, Amir H. Banihashemi, VLSI circuits: Iterative decoding in analog CMOS, Proceedings of the 13 th ACM Great Lakes Symposium on VLSI April 2003, Pages:15-20.		
	CK	Mohammad M. Mansour, Naresh R. Shanbhag, Session 11: Low-power VLSI decoder architectures for LDPC codes, Proceedings of the 2002 international symposium on Low power electronics and design August 2002, Pages: 284-289.		
	CL	R. Blahut, "Theory and Practice of Error Control Codes", Library of Congress Cataloging in Publication Data, pp-47-49, (May 1984)		
	CM	W. W. Peterson and E.J. Weldon, Jr., "Error-Correcting Codes", Second Edition, The Massachusetts Institute of Technology, pp212-213,261-263, 263, (1986)		

Examiner Signature		Date Considered	
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